

Notice of References Cited

Application/Control No.

09/938,040

Applicant(s)/Patent Under
Reexamination
HATZILAMBROU ET AL.

Examiner

Zeev Kitov

Art Unit

2836

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,744,842	04-1998	Ker	257/362
	B	US-6,404,269	06-2002	Voldman	327/534
	C	US-5,991,135	11-1999	Saleh	361/56
	D	US-5,117,129	05-1992	Hoffman et al.	307/443
	E	US-5,946,177	08-1999	Miller et al.	361/56
	F	US-5,436,183	07-1995	Davis et al.	437/40
	G	US-6,385,028	05-2002	Kouno	361/111
	H	US-6,510,033	01-2003	Maloney et al.	361/111
	I	US-5,255,146	10-1993	Miller	361/56
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Ker et al., ESD Protection Design on Analog Pin with Very Low Input Capacitance for High-Frequency or Current Mode Applications. IEEE Journal of Solid-State Circuits, vol. 35, No 8, August 2000.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.